



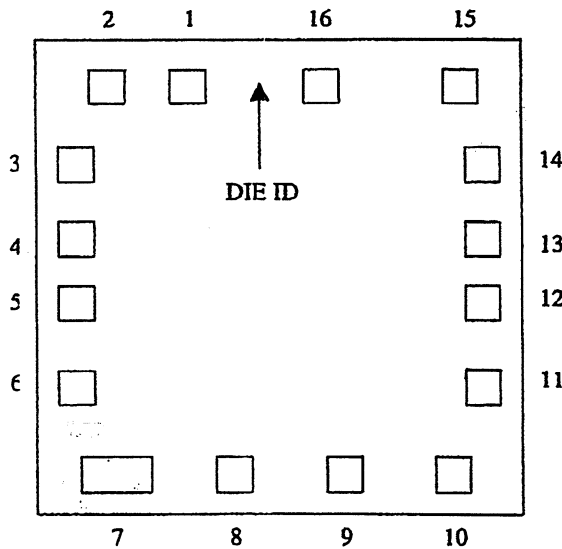
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PAD FUNCTIONS

1. CLKIN
2. VEE
3. CLK1
4. N.CLK1
5. N.CLK2
6. CLK2
7. VCCA (2 wires)
8. TCLK
9. CLKSEL
10. VCC
11. CLK3
12. N.CLK3
13. N.CLK4
14. CLK4
15. VEE
16. N.CLKIN



The information given is believed to be correct at the time of issue.

Please verify your requirements prior to commencement of any assembly process, as no liability for omission or error can be accepted.

Back potential is the electrical potential of the substrate (bulk silicon) which does not require an electrical connection unless stated in this drawing.

Note: 1 mil = 0.001inch

<u>APPROVED</u> GB DATE: 10.08.10	<p style="font-size: 2em; font-weight: bold;">100315</p> <p style="font-weight: bold;">FAIRCHILD SEMICONDUCTOR</p>	<p><u>DIE INFORMATION</u></p> <p>DIMENSIONS (Mils): 49 x 49 BOND PADS (Mils): MIN. PAD PITCH (Mils): 6.5 min MASK REF: F315Z BACK POTENTIAL:</p>
<p><u>SERIAL NUMBER</u> M000224</p>		<p><u>METALLISATION</u></p> <p>TOP: Al BACK: Si</p>